

JPW

Docket No.: 050099-0238

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shigenobu MAEDA, et al.

Application No.: 10/748,273

Filed: December 31, 2003

: Customer Number: 20277
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: Confirmation Number: 6603
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: Group Art Unit: 2823
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: Examiner: T. V. Pham
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For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE HAVING IMPURITY
REGION UNDER ISOLATION REGION (As Amended)

PRELIMINARY AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Supplemental to the Request for Continued Examination filed June 10, 2005, please
amend the above-identified application, prior to examination thereof, as follows: